

PATENTS

Attorney Docket No. 174/188

3/1/04

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Applicants : Andy L. Lee et al.
Filed : Herewith
For : SHIFT REGISTER IMPLEMENTATIONS OF
FIRST-IN/FIRST-OUT MEMORIES

New York, New York 10020
January 16, 2001

Hon. Commissioner for Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

Prior to examining the above-identified,
concurrently filed patent application, please amend the
application as follows:

In the Claims

Replace claim 19 with the following amended
version of claim 19:

19. (Amended) First-in/first-out memory circuitry
comprising:

write counter circuitry configured to count,
in a Gray code, half cycles of a write clock signal that is
synchronized with successive write data words;

read counter circuitry configured to count,
in double increments of the Gray code, a read clock signal;

first shift register circuitry configured to
shift in Gray code count data produced by the write counter
and to output a Gray code count selected based on double-
increment count data produced by the read counter circuitry;
and

second shift register circuitry configured to
shift in write data words in synchronism with the write